

Refine Search

Search Results -

Term	Documents
INCREMENT	83348
INCREMENTS	101656
DECREMENT	19172
DECREMENTS	12471
(55 AND DECREMENT AND INCREMENT).USPT.	4
(L55 AND INCREMENT AND DECREMENT).USPT.	4

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L64

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Tuesday, April 05, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

DB=USPT; PLUR=YES; OP=ADJ

L64 155 and increment and decrement
L63 L62 and decrease adj data
L62 L59 and increase adj data
L61 L59 and decrease adj samples
L60 L59 and increase adj samples
L59 L55 and counter
L58 L55 and increment adj samples
L57 L55 and working adj protocol
L56 L55 and working adj protoco

Hit Count Set Name

result set

4 L64
 0 L63
 8 L62
 0 L61
 0 L60
 12 L59
 2 L58
 0 L57
 0 L56

<u>L55</u>	L52 and first-in and first-out and protocol	16	<u>L55</u>
<u>L54</u>	L52 and FIFO adj protocol	0	<u>L54</u>
<u>L53</u>	L52 and operational adj protocol	1	<u>L53</u>
<u>L52</u>	L51 and FIFO	33	<u>L52</u>
<u>L51</u>	L50 and samples	67	<u>L51</u>
<u>L50</u>	L49 and real-time	77	<u>L50</u>
<u>L49</u>	codec and modem and circular adj buffer	148	<u>L49</u>
<u>L48</u>	L47 and decrement	1	<u>L48</u>
<u>L47</u>	L46 and increment	2	<u>L47</u>
<u>L46</u>	L44 and host adj signal	2	<u>L46</u>
<u>L45</u>	L44 and signal adj processor	11	<u>L45</u>
<u>L44</u>	L43 and real-time	13	<u>L44</u>
<u>L43</u>	L42 and sample	28	<u>L43</u>
<u>L42</u>	L40 and first-in and first-out and protocol	46	<u>L42</u>
<u>L41</u>	L40 and FIFO adj protocol	0	<u>L41</u>
<u>L40</u>	circular adj buffer and modem and counter	447	<u>L40</u>
<u>L39</u>	L37 and modem	2	<u>L39</u>
<u>L38</u>	L37 and counter	1	<u>L38</u>
<u>L37</u>	L33 and real-time	2	<u>L37</u>
<u>L36</u>	L35 and real-time	1	<u>L36</u>
<u>L35</u>	L34 and FIFO	3	<u>L35</u>
<u>L34</u>	L33 and samples	5	<u>L34</u>
<u>L33</u>	L32 and circular adj buffer	8	<u>L33</u>
<u>L32</u>	370/235.ccls.	749	<u>L32</u>
<u>L31</u>	L30 and protocol	1	<u>L31</u>
<u>L30</u>	L29 and counter	2	<u>L30</u>
<u>L29</u>	L28 and circular adj buffer	2	<u>L29</u>
<u>L28</u>	L26 and FIFO	2	<u>L28</u>
<u>L27</u>	L26 and first-in	1	<u>L27</u>
<u>L26</u>	L25 and signal adj processing	2	<u>L26</u>
<u>L25</u>	L24 and decrement adj samples	3	<u>L25</u>
<u>L24</u>	signal adj processor and increment adj samples	57	<u>L24</u>
<u>L23</u>	real-time and periodically adj transmit adj samples	0	<u>L23</u>
<u>L22</u>	L21 and modem	8	<u>L22</u>
<u>L21</u>	L20 and samples	8	<u>L21</u>
<u>L20</u>	L19 and acknowledged	8	<u>L20</u>
<u>L19</u>	L15 and interrupt	11	<u>L19</u>
<u>L18</u>	working adj protocol and 17	0	<u>L18</u>
<u>L17</u>	17 and operational adj protocol	1	<u>L17</u>
<u>L16</u>	L14 and operational adj protocol	1	<u>L16</u>
<u>L15</u>	L14 and protocol	11	<u>L15</u>

<u>L14</u>	first-in and first-out and l11	11	<u>L14</u>
<u>L13</u>	L12 and protocol	2	<u>L13</u>
<u>L12</u>	L10 and transfer adj samples	2	<u>L12</u>
<u>L11</u>	L10 and samples	45	<u>L11</u>
<u>L10</u>	L7 and real-time	46	<u>L10</u>
<u>L9</u>	L8 and real-time	1	<u>L9</u>
<u>L8</u>	L7 and transmit adj samples	3	<u>L8</u>
<u>L7</u>	L5 and signal adj processor	90	<u>L7</u>
<u>L6</u>	L5 and host adj signal adj processor	1	<u>L6</u>
<u>L5</u>	circular adj buffer and counter and modem and codec	108	<u>L5</u>
<u>L4</u>	L3	1	<u>L4</u>
<u>L3</u>	l2 and circular	1	<u>L3</u>
<u>L2</u>	L1 and counter and buffer	2	<u>L2</u>
<u>L1</u>	host adj signal adj processor and CODEC and modem	9	<u>L1</u>

END OF SEARCH HISTORY